IN THE CLAIMS

Please cancel claim 23.

Please add the following claim:

30. (new) A program storage device readable by a machine, tangibly embodying a

program of instruction executable by a machine to perform method step for creating a

model of inputs and outputs of integrated circuits, the method comprising the steps of:

representing in the model the output characteristics of integrated circuit driver circuits by

two types of elements, switching and non-switching;

tabulating the output characteristics for each of the elements by applying a DC voltage

source on the output of the driver circuits and measuring the current through each

element;

characterizing the switching elements as voltage-time controlled resistors by obtaining

the product of DC impedance as a function of voltage and scalars that are functions of

time; and

embedding in the model equations that are functions of input edge arrival times and cycle

time for each scalar in order to modify the behavior of the switching and non-switching

elements to fit their environment.

Please amend the claims as follows:

1. (currently amended) A method for creating a model of inputs and outputs of

integrated circuits, comprising the steps of:

representing in the model the output characteristics of integrated circuit driver circuits by

two types of elements, switching and non-switching;

tabulating the output characteristics for each of the elements by applying a DC voltage

source on the output of the driver circuits and measuring the current through each

element;

characterizingrepresenting in the model switching elements as voltage-time controlled

resistors by obtaining the product of DC impedance as a function of voltage and a scalars

that isare-a functions of time; and

embedding in the model equations that are functions of input edge arrival times and cycle

time for each scalar in order to modify the behavior of the switching and non-switching

elements to account for variations in environmental conditions-type.

2. (currently amended) The method of claim 1 also comprising the step of :

accounting for variations in temperature and supply voltages in the intergrated circuit, wherein DC characteristics are obtained from a dc_base according to the equation: dc_impedance = (1+D0)*dc_base, where DO is a function of supply voltage and temperature.

3. (currently amended) The method of claim 1 where the step of characterizing the switching elementsrepresenting as a voltage-time-controlled resistors also comprises the step of: normalizing transientthe time-controlled impedance to the dc impedance to produce the scalars that areis-a functions of time.

4. (currently amended) The method of claim 1 where the characterization of the switching elements as representation of the voltage-time controlled resistors is-are obtained starteding with a midpoint of the input transition of an input signal of a driver.

5. (original) The method of claim 1 also comprising the step of saving the scalars in a tabular format.

6. (currently amended) The method of claim 1 also comprising the step of- making wave-forms generated byfor the switching elements periodic by using in-definitions of local

times as functions of periodic rising and falling input edge arrival times and controlling

time through indexing equations.

7. (original) The method of claim 1 also comprising the step of applying indexing

equations to account for variations in environmental conditions.

8. (original) The method of claim 7 wherein the environmental conditions are slew rate,

temperature or supply voltage.

9. (original) The method of claim 1 where the switching elements reflect composite

transient impedance behavior of a pull-up or pull-down network that are comprised of a

plurality of FETs and parasitics.

10. (original) The method of claim 1 where the non-switching elements are an ESD

device and a power clamp.

11. (currently amended) The method of claim 1 where the method also comprisinges the

steps of obtaining behavioral characteristics for a pre-drive current stage linked to the

switching elements and a decoupling stage linked to the pre-drive current stage,

switching elements, and non-switching elements; and applying them to the model.

12. (currently amended) A method for creating a model of inputs and outputs of

integrated circuits, comprising the steps of:

representing in the model the output characteristics of integrated circuit driver circuits by two types of elements, switching and non-switching;

tabulating the output characteristics for each of the elements by applying a DC voltage source on the output of the driver circuits and measuring the current through each element;

characterizingrepresenting in the model switching elements as- voltage-time controlled resistors by obtaining the product of -DC conductance as a function of voltage and a scalars that are is a functions of time; and

embedding in the model equations that are functions of input edge arrival times and cycle time for each scalar in order to modify the behavior of the switching and non-switching elements to account for variations in environmental conditions type.

13. (currently amended) The method of claim 12 also comprising the step of :

accounting for variations in temperature and supply voltages in the integrated circuit, wherein DC characteristics are obtained from a dc_base according to the equation: $dc\_conductance = (1+D0)*dc\_base$, where D0 is a function of supply voltage and temperature.

14. (currently amended) The method of claim 12 where the step of characterizing the switching elements representing as a voltage time-controlled resistors also comprises the step of: normalizing transientthe time controlled conductance to the DCdc conductance to produce the scalar that is a function of time.

15. (currently amended) The method of claim 12 where the characterization of the switching elements asrepresentation of the voltage-time controlled resistors is obtained by starting timing atwith a midpoint of the transition of an input signal of a driver.transition.

16. (original) The method of claim 12 also comprising the step of saving the scalars in a tabular format.

17. (currently amended) The method of claim 12 also comprising the step of making wave-forms generated byfor the switching elements periodic by using in definitions of local times as functions of periodic rising and falling input edge arrival times and controlling time through indexing equations.

18. (original) The method of claim 12 also comprising the step of applying indexing equations to account for variations in environmental conditions.

19. (currently amended) The method of claim 18 wherein the environmental conditions are slew rate, temperature or supply voltage.

20. (currently amended) The method of claim 12 where the switching elements reflect composite transient conductance behavior of a pull-up or pull-down network that are comprised of a plurality of FETs and parasitics.

21. (original) The method of claim 12 where the non-switching elements are an ESD device and a power clamp.

22. (currently amended) The method of claim 12 where the method also comprisinges the steps of obtaining behavioral characteristics for a pre-drive current stage linked to the switching elements and a decoupling stage linked to the pre-drive stage, switching and non-switching elements; and applying them to the model.

23. (cancelled)

24. (currently amended) The methodcircuit of claim 2312 wherein the modelwhich also comprises a pre-drive stage coupled to the switching elements and a decoupling stage tied to the switching and non-switching elements and the pre-drive stage.

25. (currently amended) The methodcircuit of claim 24 where a fixed value element is used to represent the pre-drive or decoupling stage.

26. (currently amended) The ~~method~~circuit of claim 24 where a non-switching element that is a function of parameters that do not vary in time is used to represent the pre-drive or decoupling stage.

27. (currently amended) The ~~method~~circuit of claim 24 where a switching element which is a function of both time and non-time varying parameters is used to represent the pre-drive or decoupling stage.

28. (currently amended) A method for creating a model of inputs and outputs of integrated circuits, comprising the steps of:

representing in the model the output characteristics of integrated circuit driver circuits by two types of elements, switching and non-switching;

tabulating the output characteristics for each of the elements by applying a DC voltage source on the output of the driver circuits and measuring the current through each element;

~~representing in~~characterizing the ~~model~~switching elements as voltage-time controlled resistors by obtaining the product of DC conductance or impedance as a function of voltage and ~~a~~scalars that areis~~a~~ functions of time;

accounting for variations in input slew rate, temperature, and supply voltages~~and their~~

~~affects~~ on the elements by ~~where device turn-on characteristic~~using a modified local time

~~can be obtained from device_turn_on_base according to the equation:~~

~~device_turn_on = device_turn_on_base + (K0 + K1*max(device_turn_on_base - K2, 0)),~~

~~where K0, K1, and K2 are functions of supply voltage, input slew rate, and temperature;~~

accounting for variations in temperature and supply voltages in the integrated, wherein

DC characteristics are obtained from a dc_base, according to the equation:

dc_impedance or ~~(conductance)~~ = $(1+D0)*dc\_base$, where D0 is a function of supply

voltage and temperature; and

embedding in the model equations that are functions of input edge arrival times and cycle

time for each scalar in order to modify the behavior of the switching and non-switching

elements to account for variations in environmental conditions ~~type~~.

29. (currently amended) A method for creating a model of inputs and outputs integrated

circuits, comprising the steps of:

representing in the model the output characteristics of integrated circuit driver circuits by

two types of elements, switching and non-switching;

tabulating the output characteristics for each of the elements by applying a DC voltage source on the output of the driver and measuring the current through each element;

representing in the model switching elements as voltage-time controlled resistors by obtaining the product of DC conductance or impedance as a function of voltage and a scalars that are~is~ a~-functions of time;

accounting for variations in input slew rate, temperature, and supply voltages, and their effects on the elements by using a modified local time~-device-turn-on-characteristic-can be-obtained-from-device_turn_on_-base-according-to-the-equation:~
~device_turn_on = device_turn_on_base + (K0 + K1*max(device_turn_on_base- K2, 0)),~
~where K0, K1, and K2 are functions-of-supply -voltage, input-slew-rate, and temperature;~

accounting for variations in temperature and supply voltages, wherein device DC characteristics are~can-be~ obtained from a dc_base, according to the equation:
dc_impedance or ~(conductance)~ = (1+D0)*dc_base, where D0 is a function of supply voltage and temperature; and

embedding in the model equations that are functions of input edge arrival times and cycle time for each scalar in order to modify the behavior of the switching and non-switching elements to account for variations in environmental conditions~-type~.

30. (new) A program storage device readable by a machine, tangibly embodying a program of instruction executable by a machine, to perform method steps for creating a model of inputs and outputs of integrated circuits, the method comprising the steps of:

representing in the model the output characteristics of integrated circuit driver circuits by two types of elements, switching and non-switching;

tabulating the output characteristics for each of the elements by applying a DC voltage source on the output of the driver circuits and measuring the current through each element;

characterizing the switching elements as voltage-time controlled resistors by obtaining the product of either DC impedance or conductance as a function of voltage and scalars that are functions of time; and

embedding in the model equations that are functions of input edge arrival times and cycle time for each scalar in order to modify the behavior of the switching and non-switching elements to account for variations in environmental conditions.